

AMENDMENTS TO THE CLAIMS

This listing of claims will replace all prior versions and listings of claims in the application:

LISTING OF THE CLAIMS:

1. (Previously Presented) Method of allocating resources available on a computer system to run a plurality of program units concurrently, comprising:

a step of receiving a request for running a program unit;

a step of obtaining parameters that represent attributes of the program unit;

a step of allocating resources required to run the program unit, based on a resource allocation table and a cache management table as well as the parameters;

a step of registering results of the allocating resources into the resource allocation table; and

a step of registering an allocated storage domain of a cache memory to be used for the program unit into the cache management table;

wherein:

the parameters give positional information for a principal part to be executed at a high frequency in the program unit;

the resource allocation table has address reference information on a main storage to be used for program units as well as the program unit;

the cache management table lists addresses of the cache memory and program units mapped in cache address domains, specifying an address for the program unit; and

the step of allocating resources determines an address of area to be used for the program unit in the main storage, ensuring that the principal part of the program unit is assigned an entry address of free cache area, based on the positional information for the principal part, the address reference information, and the cache management table.

2. (Cancelled)

3. (Previously Presented) The method of allocating resources according to claim 1, wherein:

the cache management table lists pages of the cache memory and program units mapped in cache pages, specifying a page or pages for the program unit.

4. (Currently Amended) An operating system capable of running a plurality of program units concurrently and, when receiving a request for running a new program unit, the operating system carrying out:

obtaining parameters that represent attributes of the program unit;

allocating resources required to run the program unit, based on a resource allocation table and a cache management table as well as the parameters;

registering results of the allocating resources into the resource allocation table; and

registering an allocated storage domain of a cache memory to be used for the program unit into the cache management table;

wherein:

the cache memory has entry address domains, each of which is specified by an entry address;

the parameters give positional information for a principal part to be executed at a high frequency in the program unit;

the resource allocation table has address reference information on a main storage to be used for program units;

the cache management table lists addresses of the cache memory and program units mapped in cache address domains; and

the allocating resources determines an address of area to be used for the program unit in the main storage, ensuring that an entry address to be assigned for the principal part to be executed at a high frequency in the program unit differs from an entry address assigned for ~~any~~ part-parts to be executed at a high frequency in ~~any~~ all program ~~unit-units~~ other than the program unit, based on the positional information for the principal part, the address reference information, and contents of the cache management table.

5. (Cancelled)

6. (Previously Presented) The operating system according to claim 4, wherein:

the cache memory has a plurality of pages; and
the cache management table lists pages of the cache memory and program units mapped in cache pages.

7. (Previously Presented) The operating system according to claim 6, wherein:

rewriting of a cache page register is performed, according to the contents of the cache management table, when control of execution transfers from the program unit to another program unit.

8. (Currently Amended) The operating system according to claim 6, wherein:

the allocating resources includes determining the number of pages of the cache memory to be assigned for the program unit, based on execution priority information that is given by ~~some~~ a plurality of the parameters.

9. (Currently Amended) The operating system according to claim 6, wherein:

the allocating resources includes determining the number of program units to be mapped for a specific page of the cache memory, based on execution priority information that is given by ~~some~~ a plurality of the parameters.

10. (Currently Amended) A computer system including a cache memory, a CPU, and storage:

the computer system operating under the control of an operating system stored into the storage, and when the computer system receives a request for running a program unit, the operating system carrying out:

obtaining parameters that represent attributes of the program unit;

allocating resources required to run the program unit, based on a resource allocation table and a cache management table as well as the parameters;

registering results of the allocating resources into the resource allocation table; and

registering an allocated storage domain of a cache memory to be used for the program unit into the cache management table;

wherein:

the cache memory has entry address domains, each of which is specified by an entry address;

the parameters give positional information for a principal part to be executed at a high frequency in the program unit;

the resource allocation table has address reference information on a main storage to be used for program units;

the cache management table lists addresses of the cache memory and program units mapped in cache address domains; and

the allocating resources determines an address of area to be used for the program unit in the main storage, ensuring that an entry address to be assigned for the principal part to be executed at a high frequency in the program unit differs from an entry address assigned for ~~any~~ ~~part~~ parts to be executed at a high frequency in ~~any~~ all program ~~unit~~ units other than the program unit, based on the positional information for the principal part, the address reference information, and the contents of the cache management table.

11. (Cancelled)

12. (Previously Presented) The computer system according to claim 10, wherein:

the cache memory has a plurality of pages; and
the cache management table lists pages of the cache memory and program units mapped in cache pages.

13. (Currently Amended) A computer system including a CPU, a cache memory, and storage and capable of running a plurality of program units concurrently:

the computer system having a resource allocation table into which results of allocating resources are registered and a cache management table into which allocated storage domains of the cache memory are registered;

the storage including an area storing an operating system that controls the computer system; and

when running one of the plurality of program units, the operating system obtaining parameters for the program unit and allocating resources for the program unit, based on contents of the resource allocation table and the cache management table as well as the parameters;

wherein:

the cache memory has entry address domains, each of which is specified by an entry address;

the parameters give positional information for a principal part to be executed at a high frequency in the program unit;

the resource allocation table has address reference information on a main storage to be used for program units;

the cache management table lists addresses of the cache memory and program units mapped in cache address domains; and

the allocating resources determines an address of area to be used for the program unit in the main storage, ensuring that an entry address to be assigned for the principal part to be executed at a high frequency in the program unit differs from an entry address assigned for ~~any part~~ parts to be executed at a high frequency in ~~any~~ all program ~~unit~~ units other than the program unit, based on the positional information for the principal part, the address reference information, and the contents of the cache management table.

14. (Cancelled)

15. (Previously Presented) The computer system according to claim 13, wherein:

the cache memory has a plurality of pages; and
the cache management table lists pages of the cache memory and program units mapped in cache pages.

Claims 16-21 (Cancelled)

22. (Currently Amended) The operating system according to claim 7, wherein:

the allocating resources includes determining the number of pages of the cache memory to be assigned for the program unit, based on execution priority information that is given by ~~some~~ a plurality of the parameters.

23. (Currently Amended) The operating system according to claim 7, wherein:

the allocating resources includes determining the number of program units to be mapped for a specific page of the cache memory, based on execution priority information that is given by ~~some~~ a plurality of the parameters.